Analog Switched-Capacitor Low-Pass Filter (SC LPF) Using Different Approximations (Butterworth, Chebyshev and Elliptic)

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Abstract
This work demonstrates the process of designing and simulating analog low-pass filters according to some given specifications, their conversion to active filters and switched-capacitor filters are also provided.

1. Prototype filter design
1.1 The task
It is necessary to synthesize a low-pass filter with the following characteristics by the method of elemental simulation:

\[ F_c = 5 \text{ kHz} \] – Cutoff frequency
\[ F_s = 6 \text{ kHz} \] – Stopband frequency
\[ a_s \geq 30 \text{ dB} \] – Stopband attenuation
\[ \Delta \leq 0.2 \text{ dB} \] – Passband ripple

1.2 Prototype filter design
The following are operations for normalizing the cutoff frequency and determining the reflection coefficient.

Normalization:
\[ \Omega_s = \frac{F_s}{F_D} = \frac{6000}{5000} = 1.2 \]

Determining the reflection coefficient
\[ \rho = \sqrt{1 - e^{-0.23026a_D}} = 0.212 \]
\[
\alpha(\rho) = 10 \log(\rho^{-2} - 1) = 13.27 \text{ dB}
\]
\[
a_s + \alpha(\rho) = 30 + 13.27 = 43.27 \text{ dB}
\]
\[
\frac{1}{\Omega_s} = \frac{1}{1,2} = 0,83333
\]
Next, let us estimate the filter order according to the dependency graphs \(a_s + \alpha(\rho)\) on \(\Omega_s\):

- For Butterworth approximation filter: filter order is 25 (maximum is 15)
- For Chebyshev approximation filter: filter order is 9
- For Zolotarev-Cauer /elliptic approximation: filter order is 5

The normalized values of ladder elements are shown in the table below:

<table>
<thead>
<tr>
<th>(C_P)</th>
<th>(L_P)</th>
<th>(C_T)</th>
<th>(L_T)</th>
<th>(C_C)</th>
<th>(L_C)</th>
<th>(C_{PC})</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.188043</td>
<td>0.555911</td>
<td>1.34884</td>
<td>1.40489</td>
<td>1.066663</td>
<td>1.059602</td>
<td>0.312879</td>
</tr>
<tr>
<td>0.889483</td>
<td>1.203743</td>
<td>2.274466</td>
<td>1.550862</td>
<td>1.494707</td>
<td>0.633204</td>
<td>1.026636</td>
</tr>
<tr>
<td>1.455394</td>
<td>1.843437</td>
<td>2.339126</td>
<td>1.550862</td>
<td>0.690164</td>
<td></td>
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</tr>
<tr>
<td>1.759654</td>
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</table>

**Table 1. Normalized values of the ladder circuit**

It is necessary to de-normalize the normalized values. we assume that:

\[
R_s = R_l = 1\text{K Ohms}
\]
\[
n_z = 1000
\]
\[
n_\omega = 2\pi F_C = 31415 \text{ rad/sec}
\]

An example of calculating the values (denormalization) for cauer filter:

\[
C_{C_1} = C_{kC_1} \frac{1}{n_\omega n_z} = 1.066663 * 31.85 \text{ nF} = 33.95 \text{ nF}
\]
\[ C_{C,2} = C_{kC,2} \frac{1}{n_\omega n_z} = 1,494707 * 31,85 \text{ nF} = 47,56 \text{ nF} \]

\[ C_{C,3} = C_{kC,3} \frac{1}{n_\omega n_z} = 0,690164 * 31,85 \text{ nF} = 21,97 \text{ nF} \]

\[ L_{C,1} = L_{kC,1} \frac{n_z}{n_\omega} = 1,059602 * 31,85 \text{ mH} = 33,73 \text{ mH} \]

\[ L_{C,2} = L_{kC,2} \frac{n_z}{n_\omega} = 0,633204 * 31,85 \text{ mH} = 20,16 \text{ mH} \]

\[ C_{pC,1} = C_{kpC,1} \frac{1}{n_\omega n_z} = 0,312879 * 31,85 \text{ nF} = 9,96 \text{ nF} \]

\[ C_{pC,2} = C_{kpC,2} \frac{1}{n_\omega n_z} = 1,026636 * 31,85 \text{ nF} = 32,68 \text{ nF} \]

The table below shows the actual (de-normalized) values of ladder elements.

<table>
<thead>
<tr>
<th>( C_p ), nF</th>
<th>( L_p ), mH</th>
<th>( C_T ), nF</th>
<th>( L_T ), mH</th>
<th>( C_C ), nF</th>
<th>( L_C ), mH</th>
<th>( C_{pC} ), nF</th>
</tr>
</thead>
<tbody>
<tr>
<td>5.985594593</td>
<td>17.69519671</td>
<td>42.93491069</td>
<td>44.7190376</td>
<td>33.95293781</td>
<td>33.7281792</td>
<td>9.959247888</td>
</tr>
<tr>
<td>28.31312325</td>
<td>38.31632973</td>
<td>72.39850136</td>
<td>49.36547067</td>
<td>47.5780015</td>
<td>20.15550932</td>
<td>32.67883883</td>
</tr>
<tr>
<td>46.32662985</td>
<td>58.67842217</td>
<td>74.45669308</td>
<td>49.36547067</td>
<td>21.96860243</td>
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<td></td>
</tr>
<tr>
<td>56.01152645</td>
<td>57.26286627</td>
<td>72.39850136</td>
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</table>

Table 2. De-normalized values of the ladder circuit

The schemes of all the three filters are shown below:

![Diagram of ladder circuit](image-url)
And below is shown the magnitude response for the three types of filters given above.

Figure 2 – Magnitude response of the three passive filters

Figure 3 shows the maximum values of flatness in the passband of prototype filters.

Figure 3 – Maximum values of flatness in the passband of prototype filters
The results from filters prototypes satisfy the requirement - $\Delta \leq 0.2$ dB.

Figure 4 below shows the magnitude response values of the prototype filters at the passband and stopband frequencies.

![Magnitude response values of the filters at pass and stop band frequencies](image)

Figure 4 – Magnitude response values of the filters at pass and stop band frequencies

Two filters, namely (Chebyshev and Cauer) satisfiy the condition - $a_S \geq 30$ dB. Butterworth doesn’t which is expected because the filter order is 15 and to achieve $a_S \geq 30$ dB then we need to use order 25 butterworth filter, which is not practical to implement.

The figures below (5,6 and 7) show pole-zero diagram for all the three filters (Butterworth, Chebyshev and Cauer).
Figure 5 – Pole-zero diagram for Butterworth filter (order 15)

Figure 6 – Pole-zero diagram for Chebyshev filter (order 9)
2. Synthesis of ARC filter based on LPF prototype

2.1 Realization of active RC filter

To implement the ARC filter, we use the prototype for the Chebyshev approximation presented in Figure 9, as well as Cauer approximation presented in Figure 12.

Transfer functions of the scheme components are given below:

\[
Y_1 = \frac{1}{R_s + pL_1} \quad \quad \quad T_1 = \frac{1}{\frac{R_s}{R_n} + \frac{pL_1}{R_n}}
\]
<table>
<thead>
<tr>
<th>$Z_2 = \frac{1}{pC_2}$</th>
<th>$T_2 = -\frac{1}{pC_2R_n}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$Y_3 = \frac{1}{pL_3}$</td>
<td>$T_3 = \frac{1}{pL_3}$</td>
</tr>
<tr>
<td>$Z_4 = \frac{1}{pC_4}$</td>
<td>$T_4 = -\frac{1}{pC_4R_n}$</td>
</tr>
<tr>
<td>$Y_5 = \frac{1}{pL_5}$</td>
<td>$T_5 = \frac{1}{pL_5}$</td>
</tr>
<tr>
<td>$Z_6 = \frac{1}{pC_6}$</td>
<td>$T_6 = -\frac{1}{pC_6R_n}$</td>
</tr>
<tr>
<td>$Y_7 = \frac{1}{pL_7}$</td>
<td>$T_7 = \frac{1}{pL_7}$</td>
</tr>
<tr>
<td>$Z_8 = \frac{1}{pC_8}$</td>
<td>$T_8 = -\frac{1}{pC_8R_n}$</td>
</tr>
<tr>
<td>$Y_9 = \frac{1}{R_l + pL_9}$</td>
<td>$T_9 = \frac{1}{R_l + pL_9}$</td>
</tr>
</tbody>
</table>

Using the formulas shown above, the circuit below was implemented using operational amplifiers, resistors and capacitors:

![Figure 9 - Structure of the active Chebyshev RC (ARC) filter](image-url)
Figure 10 – Amp. Response comparison of active and passive Chebyshev filter

And below (Figure 11) shows that the amplitude-frequency characteristics completely coincide with the previous passive filter design and the given condition in the task, the condition is passband ripple $\Delta \leq 0.2 \text{ dB}$, this means the design of the ARC filter is successful.

Figure 11 - Unevenness in the passband frequency range (Chebyshev)
Figure 12 - Structure of the filter prototype (Cauer)

Figure 13 - Structure of the active Cauer RC (ARC) filter

Figure 14 - Amp. Response comparison of active and passive Cauer filter
Figure 15 - Unevenness in the passband frequency range (Cauer)

3. Switched capacitor LPF

The clock frequency is chosen equal to 1 MHz. For 1 MHz, the capacitance used to replace the resistors is:

\[ C = \frac{1}{fR} = \frac{1}{10^6 \times 10^3} = 1 \times 10^{-9} = 1 \text{ nF} \]

Using Laker's models, the transfer functions of each part were obtained and the values of capacitors in the feedback of integrators were calculated:

\[ H_{11}^1(z) = \frac{C_1 z^{-1}}{C_2 + C_5 - C_2 z^{-1}} \approx \frac{1}{C_5 + T_c C_2 + C_5 p} = T_{1arc}(p) = \frac{1}{R \frac{R}{R_0} + pCR} \]

Inverting and non-inverting integrators:

\[ H_{23}^1(z) = \frac{C_3 z^{-1}}{C_4 - C_4 z^{-1}} \approx \frac{1}{T_c C_4 p} = T_{2arc}(p) = \frac{1}{pCR} \]

Figures 16 – 19 below show different segments of the switched capacitor filter.
Figure 16 – First segment of the SC filter

Figure 17 – Second segment of the SC filter
Figure 18 – Third segment of the SC filter

Figure 19 – Fourth (last) segment of the SC filter
To control (drive) the SC filter switches, it is necessary to generate two clock signals shifted relative to each other by a quarter of a period. Switching frequency is 1 MHz. Figure 20 shows the timing diagrams of the clock signals.

The input source is the sum of harmonic signals with frequencies from 0.5K to 8 KHz in 500 Hz steps. Figure 21 shows a schematic of the input source.

Figure 20 – The input clock signals (p1 and p2)

Figure 21 – The input signal sources (sum of harmonics)
Figure 22 – The input signal (in time domain)

Figure 23 – Spectrum of the input signal (harmonics)

Figure 24 – Active RC filter output in time domain
Figure 25 – Switched capacitor filter output in time domain

Figure 26 – Switched capacitor filter amplitude response

Figure 27 – Switched capacitor output spectrum (harmonics)
4. Conclusion

In conclusion, several different LPF configurations were taken into consideration in this student work, different low-pass filters (active, passive and switched capacitor LPF) were designed and implemented meeting the technical requirements given in the individual task (please refer to page 3).

5. References


2. Rudolf Saal, Handbook of filter design, Berlin, Germany, 1983.