

Bandpass Filter Design Using Lumped Elements for RF Applications

Sana Fatima

Department of Electrical Engineering, Lahore College for Women University (LCWU), Lahore, Pakistan

1. Abstract

In this article, we have discussed the complete method to design an RF Bandpass filter using coupled lines topology. The article includes the detailed techniques of entire design process, its schematics, outputs, and all relevant design issues. On hardware that is appropriate for the circumstances, Band pass filters in particular will be constructed, and VNA findings will be attached. We'll also talk about the differences between both software and hardware.

2. Introduction

A microstrip bandpass filter is a type of radio frequency (RF) filter that is made using microstrip transmission lines on a printed circuit board (PCB). It is used to pass a specific range of frequencies while rejecting frequencies outside of this range.

Microstrip bandpass filters are widely used in a variety of applications, including wireless communication systems, satellite systems, radar systems, and more. They have several advantages over other types of RF filters, such as low cost, low insertion loss, and small size.

To design a microstrip bandpass filter, the designer must first specify the center frequency and bandwidth of the desired passband. The filter can then be designed using a combination of microstrip transmission lines, resonators, and other components. The transmission lines and resonators can be either open- or short-circuited, depending on the desired frequency response.

One of the main challenges in designing microstrip bandpass filters is achieving a steep transition between the passband and the stopband, as well as good out-of-band rejection. This requires careful analysis and optimization of the filter design.

Microstrip bandpass filters are an important component in a variety of RF systems, and their design requires a combination of engineering knowledge and expertise in RF circuit design.

3. Band Pass Filter

3.1. Specifications

Design a band pass filter at 10.5 GHz. The filter is designed on RO3010 substrate with a dielectric thickness of 0.025 inches. The filter should have a pass band of 9.98 to 11.03 GHz. It should achieve at least 20 dB rejection at 9.65 GHz.

$$F_o = 10.5 \text{ GHz}$$

$$F_1 = 9.98 \text{ GHz}$$

$$F_2 = 11.03 \text{ GHz}$$

$$IL = 20 \text{ dB}$$

$$F = 9.65 \text{ GHz}$$

3.2. Design

We have used the following equations to calculate the order of the filter.

$$\bullet \quad P_{LR} = 1 + k^2 T_n^2 \left(\frac{\omega}{\omega_c} \right)^{2N}$$

$$\bullet \quad P_{LR} = 1 + \frac{k^2}{4} \left(\frac{2\omega}{\omega_c} \right)^{2N}$$

- $IL = 10 \log(P_{LR})$
- $20 = 10 \log(P_{LR})$
- $P_{LR} = 100$
- $100 = 1 + \frac{1^2}{4} \left(\frac{2(6.6 \cdot 10^{10})}{6.06 \cdot 10^{10}} \right)^{2N}$
- $N = 3.85$
- Choosing $N = 5$ for 0.5dB equal ripple response

Table 1: Normalized impedance values for 0.5 dB equal ripple response

N	0.5 dB Ripple										
	g ₁	g ₂	g ₃	g ₄	g ₅	g ₆	g ₇	g ₈	g ₉	g ₁₀	g ₁₁
1	0.6986	1.0000									
2	1.4029	0.7071	1.9841								
3	1.5963	1.0967	1.5963	1.0000							
4	1.6703	1.1926	2.3661	0.8419	1.9841						
5	1.7058	1.2296	2.5408	1.2296	1.7058	1.0000					
6	1.7254	1.2479	2.6064	1.3137	2.4758	0.8696	1.9841				
7	1.7372	1.2583	2.6381	1.3444	2.6381	1.2583	1.7372	1.0000			
8	1.7451	1.2647	2.6564	1.3590	2.6964	1.3389	2.5093	0.8796	1.9841		
9	1.7504	1.2690	2.6678	1.3673	2.7239	1.3673	2.6678	1.2690	1.7504	1.0000	
10	1.7543	1.2721	2.6754	1.3725	2.7392	1.3806	2.7231	1.3485	2.5239	0.8842	1.9841

Using above table, we have taken these values

$g_1 = 1.7058$, $g_2 = 1.2296$, $g_3 = 2.5408$, $g_4 = 1.2296$, $g_5 = 1.7058$, $g_6 = 1.0$

3.2.1. Lumped Elements

After choosing values of g we can proceed further. To convert a low pass into band pass filter a capacitor can be changed into a parallel combination of inductor and capacitor and an inductor can be changed into series combination of inductor and capacitor as shown in figure below.

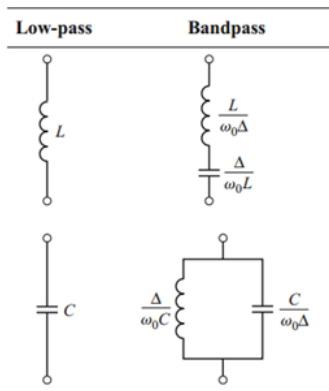


Figure 1: Low pass to band pass conversion

Where $\delta = \frac{\omega_2 - \omega_1}{\omega_0}$

3.2.1.1. Calculations

- $Z_0 = 50$; $f_c = 10.5 \cdot 10^9$; $f_1 = 9.98 \cdot 10^9$; $f_2 = 11.03 \cdot 10^9$; $N = 5$;
- $g_n = [1.7058 \ 1.22296 \ 2.5408 \ 1.2296 \ 1.7058 \ 1.0]$; $\delta = (f_2 - f_1)/f_c$;
- for $i = 1:N$
- if(mod(i,2) ~= 0)
- $C(i) = g_n(i)/(\delta \cdot Z_0 \cdot 2 \cdot \pi \cdot f_c)$; $L(i) = (Z_0 \cdot \delta)/(g_n(i) \cdot 2 \cdot \pi \cdot f_c)$;
- end
- if(mod(i,2) == 0)
- $L(i) = (Z_0 \cdot g_n(i))/(\delta \cdot 2 \cdot \pi \cdot f_c)$; $C(i) = \delta/(Z_0 \cdot g_n(i) \cdot 2 \cdot \pi \cdot f_c)$;
- end
- End

After running this code, we get following results

- $C =$
- $1.0e-11 \cdot$
- $0.5171 \ 0.0025 \ 0.7702 \ 0.0025 \ 0.5171$
- $L =$
- $1.0e-08 \cdot$
- $0.0044 \ 0.9269 \ 0.0030 \ 0.9319 \ 0.0044$

3.2.1.2. ADS Schematics

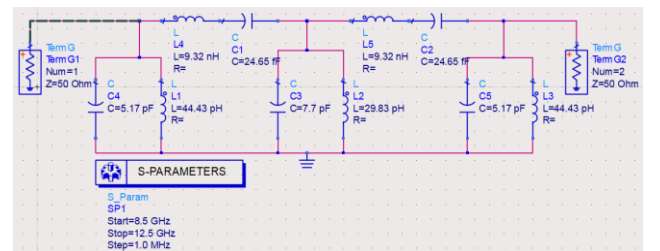


Figure 2: ADS Schematic for band pas filter using lumped elements

3.2.1.3. Results

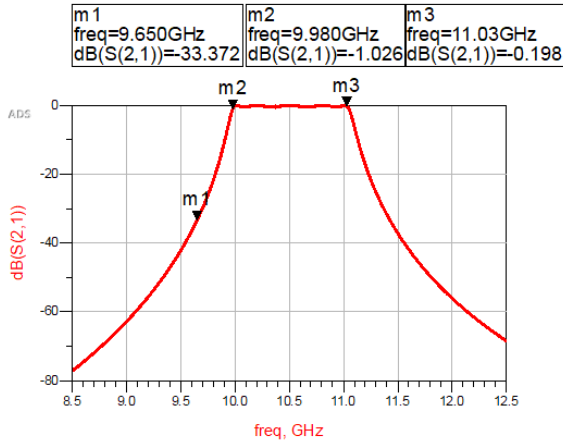


Figure 3: Results of band pass filter using lumped elements

3.2.2. Stub Technique

We know that band pass filter is a combination of series and parallel resonators so these resonators can be transformed into open and short circuit stubs. Band pass filter can also be designed by using short circuit stubs.

3.2.2.1. Calculations

- $Z_0 = 50$;
- $f_c = 10.5 \times 10^9$;
- $f_1 = 9.98 \times 10^9$;
- $f_2 = 11.03 \times 10^9$;
- $g_n = [1.7058 \ 1.2296 \ 2.5408 \ 1.2296 \ 1.7058 \ 1.0]$;
- $\Delta = (f_2 - f_1) / f_c$;
- $Z_{on} = (\pi \cdot Z_0 \cdot \Delta) / (4 \cdot g_n)$

After running the code, we get the following results

- $Z_{on} =$
- | | | | |
|--------|--------|--------|--------|
| 2.3021 | 3.1937 | 1.5456 | 3.1937 |
| 2.3021 | 3.9270 | | |

3.2.2.2. ADS Schematics

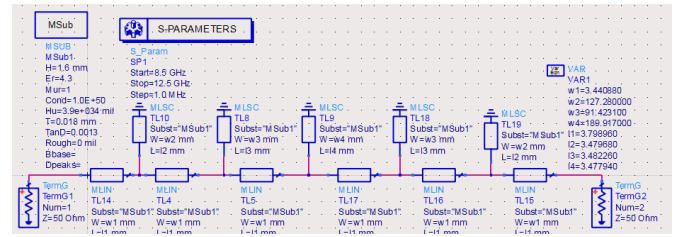


Figure 4: ADS Schematics of band pass filter using stubs

3.2.2.3. Results

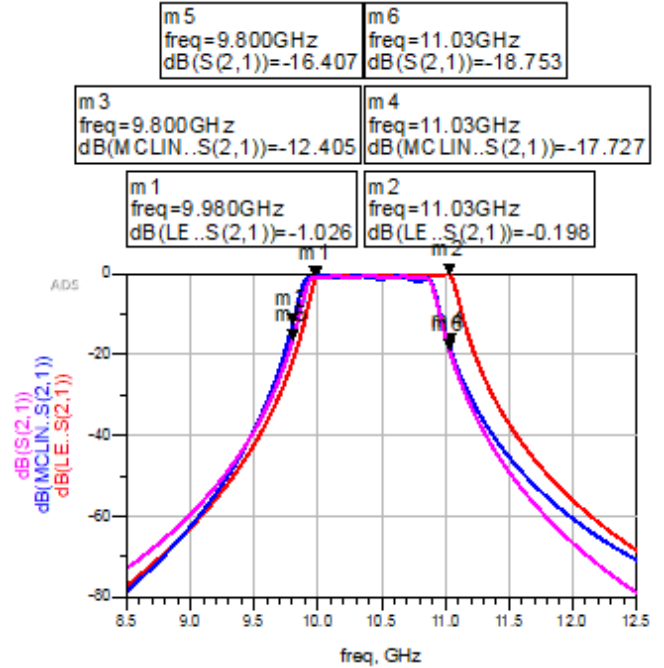
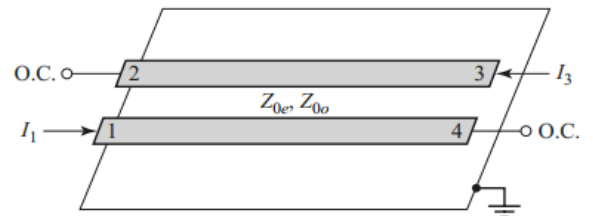


Figure 5: Results of band pass filter using stubs

3.2.3. Coupled Lines



$$Z_0 J_1 = \sqrt{\frac{\pi \Delta}{2g_1}},$$

$$Z_0 J_n = \frac{\pi \Delta}{2\sqrt{g_n - 1g_n}} \quad \text{for } n = 2, 3, \dots, N,$$

$$Z_0 J_{N+1} = \sqrt{\frac{\pi \Delta}{2g_N g_{N+1}}}.$$

$$Z_{0e} = Z_0 [1 + J Z_0 + (J Z_0)^2],$$

$$Z_{0o} = Z_0 [1 - J Z_0 + (J Z_0)^2].$$

3.2.3.1. Calculations

- $Z_0 = 50$; $f_c = 10.5 \times 10^9$; $f_1 = 9.98 \times 10^9$; $f_2 = 11.03 \times 10^9$; $N = 5$;
- $g_n = [1.7058 \ 1.22296 \ 2.5408 \ 1.2296 \ 1.7058 \ 1.0]$;
- $\Delta = (f_2 - f_1) / f_c$; $Z_{oJ}(1) = \sqrt{\pi \Delta / (2 g_n(1))}$;
- for $i = 2:N$
- $Z_{oJ}(i) = (\pi \Delta) / (2 \sqrt{g_n(i) g_n(i-1)})$;
- end
- $Z_{oJ}(N+1) = \sqrt{\pi \Delta / (2 g_n(N+1) g_n(N))}$;
- for $i = 1:N+1$
- $Z_{oe}(i) = Z_0 \cdot (1 + Z_{oJ}(i) + (Z_{oJ}(i))^2)$;
- $Z_{oo}(i) = Z_0 \cdot (1 - Z_{oJ}(i) + (Z_{oJ}(i))^2)$;
- end
- Z_{oJ}
- Z_{oe}
- Z_{oo}

After running the code, we get the following results

- $Z_{oJ} =$
- 0.3035 0.1088 0.0891 0.0889
0.1085 0.3035
- $Z_{oe} =$
- 69.7771 56.0291 54.8526 54.8384
56.0112 69.7771
- $Z_{oo} =$
- 39.4315 45.1536 45.9415 45.9514
45.1651 39.4315

3.2.3.2. ADS Schematics

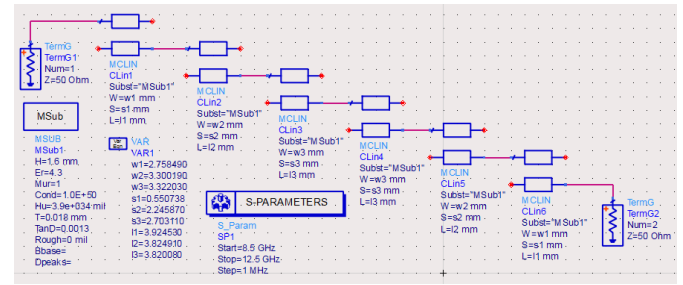


Figure 6: ADS Schematics of band pass filter using coupled lines

3.2.3.3. Results

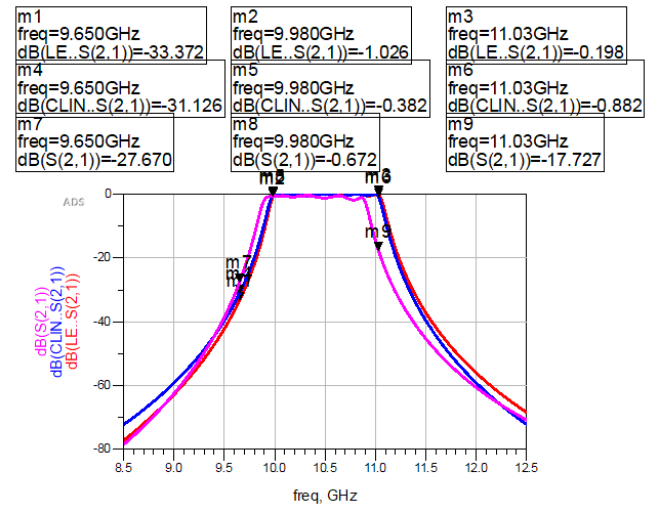


Figure 7: results of band pass filter using coupled lines

3.2.3.4. Problems

At low frequencies lengths of transmission lines can be very high that they are difficult to fabricate. Also, for low impedances width can be very low that it is difficult to fabricate.

3.3. Hardware Design

Since every design have some limitations so it was appropriate to design band pass filter using coupled lines.

3.3.1. Hardware

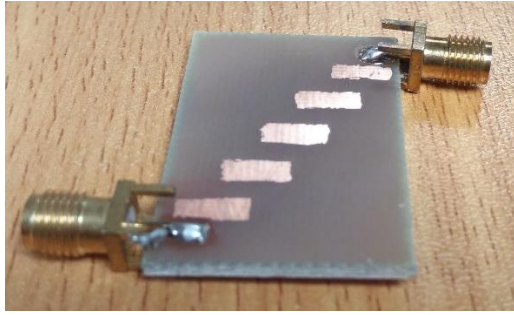


Figure 8: Hardware of band pass filter using coupled lines

3.3.2. Results

Our design was at center frequency 10.5 GHz but maximum frequency in VNA available is 6 GHz so it is impossible to test this hardware at VNA available.

4. Conclusion

In this paper, a bandpass coupled line filter is designed and fabricated. A complete design calculation method and designed flow have been discussed. The filter is designed on RO3010 substrate and then tested using VNA. It is shown that results are quite promising, and this could be used in many RF applications.

5. References

- [1] S. Li, L. D. Xu, and S. Zhao, "The internet of things: a survey," *Information systems frontiers*, vol. 17, no. 2, pp. 243-259, 2015.
- [2] K. Rose, S. Eldridge, and L. Chapin, "The internet of things: An overview," *The internet society (ISOC)*, vol. 80, pp. 1-50, 2015.
- [3] Y. Massoud and A. Nieuwoudt, "Accurate resistance modeling for carbon nanotube bundles in VLSI interconnect," in *2006 Sixth IEEE Conference on Nanotechnology*, 2006, vol. 1: IEEE, pp. 288-291.
- [4] S. Kirolos and Y. Massoud, "Adaptive SRAM design for dynamic voltage scaling VLSI systems," in *2007 50th Midwest Symposium on Circuits and Systems*, 2007: IEEE, pp. 1297-1300.
- [5] A. Nieuwoudt and Y. Massoud, "Optimizing the design of tunable spiral inductors for on-chip wireless applications," in *2006 IEEE Annual Wireless and Microwave Technology Conference*, 2006: IEEE, pp. 1-5.
- [6] F. H. Raab *et al.*, "RF and microwave power amplifier and transmitter technologies-Part 1," *high frequency electronics*, vol. 2, no. 3, pp. 22-36, 2003.
- [7] J. Joung, C. K. Ho, K. Adachi, and S. Sun, "A survey on power-amplifier-centric techniques for spectrum-and energy-efficient wireless communications," *IEEE Communications Surveys & Tutorials*, vol. 17, no. 1, pp. 315-333, 2014.
- [8] S. Kirolos *et al.*, "Analog-to-information conversion via random demodulation," in *2006 IEEE Dallas/CAS Workshop on Design, Applications, Integration and Software*, 2006: IEEE, pp. 71-74.
- [9] A. Nieuwoudt and Y. Massoud, "Assessing the implications of process variations on future carbon nanotube bundle interconnect solutions," in *8th International Symposium on Quality Electronic Design (ISQED'07)*, 2007: IEEE, pp. 119-126.
- [10] Y. Massoud, A. Nieuwoudt, and T. Ragheb, "Automated design solutions for fully integrated narrow-band low noise amplifiers," in *2006 6th International Workshop on System on Chip for Real Time Applications*, 2006: IEEE, pp. 109-114.
- [11] M. Bansal, "Low Noise Amplifier in Smart Healthcare Applications," in *2019 6th International Conference on Signal Processing and Integrated Networks (SPIN)*, 2019: IEEE, pp. 1002-1007.
- [12] A. Nieuwoudt, M. S. McCorquodale, R. T. Borno, and Y. Massoud, "Efficient analytical modeling techniques for rapid integrated spiral inductor prototyping," in *Proceedings of the IEEE 2005 Custom Integrated Circuits Conference*, 2005., 2005: IEEE, pp. 281-284.
- [13] T. Ragheb, A. Nieuwoudt, and Y. Massoud, "Efficient modeling of integrated narrow-band low noise amplifiers for design space exploration," in *Proceedings of the 16th ACM Great Lakes symposium on VLSI*, 2006, pp. 187-191.
- [14] A. Nieuwoudt and Y. Massoud, "Efficient modeling of substrate eddy currents for integrated spiral inductor design automation," in *48th Midwest Symposium on Circuits and Systems*, 2005., 2005: IEEE, pp. 1823-1826.
- [15] C. Y. Law and A.-V. Pham, "A high-gain 60GHz power amplifier with 20dBm output power in 90nm CMOS," in *2010 IEEE International Solid-State Circuits Conference-(ISSCC)*, 2010: IEEE, pp. 426-427.
- [16] Y. Massoud and J. White, "FastMag: a 3-D magnetostatic inductance extraction program for structures with permeable materials," in *Proceedings of the 2002 IEEE/ACM international conference on Computer-aided design*, 2002, pp. 478-484.
- [17] A. Hosseini, T. Ragheb, and Y. Massoud, "A fault-aware dynamic routing algorithm for on-chip networks," in *2008 IEEE International Symposium on Circuits and Systems*, 2008: IEEE, pp. 2653-2656.
- [18] T. Ragheb *et al.*, "Implementation models for analog-to-information conversion via random sampling," in *2007 50th Midwest Symposium on Circuits and Systems*, 2007: IEEE, pp. 325-328.
- [19] J. Kang *et al.*, "A highly linear and efficient differential CMOS power amplifier with harmonic control," *IEEE Journal of Solid-State Circuits*, vol. 41, no. 6, pp. 1314-1322, 2006.
- [20] Y. Massoud and J. White, "Improving the generality of the fictitious magnetic charge approach to computing inductances in the presence of permeable materials," in *Proceedings of the 39th annual Design Automation Conference*, 2002, pp. 552-555.
- [21] Y. Massoud, S. Majors, T. Bustami, and J. White, "Layout techniques for minimizing on-chip interconnect self inductance," in *Proceedings of the 35th annual Design Automation Conference*, 1998, pp. 566-571.
- [22] A. Nieuwoudt and Y. Massoud, "Performance implications of inductive effects for carbon-nanotube bundle interconnect," *IEEE electron device letters*, vol. 28, no. 4, pp. 305-307, 2007.
- [23] S. Kirolos, Y. Massoud, and Y. Ismail, "Power-supply-variation-aware timing analysis of synchronous systems," in *2008 IEEE International Symposium on Circuits and Systems*, 2008: IEEE, pp. 2418-2421.
- [24] N. O. Sokal, "RF power amplifiers, classes A through S-how they operate, and when to use each," in *Professional Program Proceedings. Electronic Industries Forum of New England*, 1997: IEEE, pp. 179-252.
- [25] Y. Massoud, S. Majors, J. Kawa, T. Bustami, D. MacMillen, and J. White, "Managing on-chip inductive effects," *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 10, no. 6, pp. 789-798, 2002.
- [26] M. Mondal *et al.*, "Mitigating thermal effects on clock skew with dynamically adaptive drivers," in *8th International Symposium on Quality Electronic Design (ISQED'07)*, 2007: IEEE, pp. 67-72.
- [27] J. Laska *et al.*, "Random sampling for analog-to-information conversion of wideband signals," in *2006 IEEE Dallas/CAS Workshop on Design, Applications, Integration and Software*, 2006: IEEE, pp. 119-122.
- [28] A. Nieuwoudt and Y. Massoud, "RC circuit model for multi-walled carbon nanotubes," in *2007 7th IEEE Conference on Nanotechnology (IEEE NANO)*, 2007: IEEE, pp. 660-663.
- [29] M. Mondal and Y. Massoud, "Reducing pessimism in RLC delay estimation using an accurate analytical frequency dependent model for inductance," in *ICCAD-2005. IEEE/ACM International Conference on Computer-Aided Design*, 2005., 2005: IEEE, pp. 691-696.
- [30] K. Chen and D. Peroulis, "Design of highly efficient broadband class-E power amplifier using synthesized low-pass matching

- networks," *IEEE Transactions on Microwave Theory and Techniques*, vol. 59, no. 12, pp. 3162-3173, 2011.
- [31] Y. Massoud, J. Kawa, D. MacMillen, and J. White, "Modeling and analysis of differential signaling for minimizing inductive crosstalk," in *Proceedings of the 38th annual Design Automation Conference*, 2001, pp. 804-809.
- [32] H. Nejati, T. Ragheb, A. Nieuwoudt, and Y. Massoud, "Modeling and design of ultrawideband low noise amplifiers with generalized impedance matching networks," in *2007 IEEE International Symposium on Circuits and Systems*, 2007: IEEE, pp. 2622-2625.
- [33] T. Ragheb, A. Nieuwoudt, and Y. Massoud, "Modeling of 3.1-10.6 GHz CMOS filter-based low noise amplifier for ultra-wideband receivers," in *2006 IEEE Annual Wireless and Microwave Technology Conference*, 2006: IEEE, pp. 1-5.
- [34] A. Nieuwoudt and Y. Massoud, "Multi-level approach for integrated spiral inductor optimization," in *Proceedings of the 42nd annual Design Automation Conference*, 2005, pp. 648-651.
- [35] S. Pfetsch *et al.*, "On the feasibility of hardware implementation of sub-Nyquist random-sampling based analog-to-information conversion," in *2008 IEEE International Symposium on Circuits and Systems*, 2008: IEEE, pp. 1480-1483.
- [36] A. Nieuwoudt and Y. Massoud, "On the impact of process variations for carbon nanotube bundles for VLSI interconnect," *IEEE Transactions on Electron Devices*, vol. 54, no. 3, pp. 446-455, 2007.
- [37] S. Kirolos, T. Ragheb, J. Laska, M. F. Duarte, Y. Massoud, and R. G. Baraniuk, "Practical issues in implementing analog-to-information converters," in *2006 6th International Workshop on System on Chip for Real Time Applications*, 2006: IEEE, pp. 141-146.
- [38] A. Nieuwoudt, M. Mondal, and Y. Massoud, "Predicting the performance and reliability of carbon nanotube bundles for on-chip interconnect," in *2007 Asia and South Pacific Design Automation Conference*, 2007: IEEE, pp. 708-713.
- [39] T. Ragheb, J. N. Laska, H. Nejati, S. Kirolos, R. G. Baraniuk, and Y. Massoud, "A prototype hardware for random demodulation based compressive analog-to-digital conversion," in *2008 51st Midwest Symposium on Circuits and Systems*, 2008: IEEE, pp. 37-40.
- [40] Y. Massoud and J. White, "Simulation and modeling of the effect of substrate conductivity on coupling inductance and circuit crosstalk," *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 10, no. 3, pp. 286-291, 2002.
- [41] J. N. Laska, S. Kirolos, M. F. Duarte, T. S. Ragheb, R. G. Baraniuk, and Y. Massoud, "Theory and implementation of an analog-to-information converter using random demodulation," in *2007 IEEE International Symposium on Circuits and Systems*, 2007: IEEE, pp. 1959-1962.
- [42] A. Nieuwoudt and Y. Massoud, "Variability-aware multilevel integrated spiral inductor synthesis," *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, vol. 25, no. 12, pp. 2613-2625, 2006.
- [43] H. G. Bae, R. Negra, S. Boumaiza, and F. M. Ghannouchi, "High-efficiency GaN class-E power amplifier with ecompact harmonic-suppression network," in *2007 European Conference on Wireless Technologies*, 2007: IEEE, pp. 375-378.
- [44] P. Alinikula, K. Choi, and S. I. Long, "Design of class E power amplifier with nonlinear parasitic output capacitance," *IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing*, vol. 46, no. 2, pp. 114-119, 1999.
- [45] T. Ragheb and Y. Massoud, "On the modeling of resistance in graphene nanoribbon (GNR) for future interconnect applications," in *2008 IEEE/ACM International Conference on Computer-Aided Design*, 2008: IEEE, pp. 593-597.
- [46] A. Nieuwoudt and Y. Massoud, "On the optimal design, performance, and reliability of future carbon nanotube-based interconnect solutions," *IEEE transactions on electron devices*, vol. 55, no. 8, pp. 2097-2110, 2008.
- [47] Y. Massoud, M. Alam, and A. Nieuwoudt, "On the selection of spectral zeros for generating passive reduced order models," in *2006 6th International Workshop on System on Chip for Real Time Applications*, 2006: IEEE, pp. 160-164.
- [48] W. Zhang, G. Asplund, A. Aberg, U. Jonsson, and O. Loof, "Active DC filter for HVDC system-a test installation in the Konti-Skan DC link at Lindome converter station," *IEEE transactions on power delivery*, vol. 8, no. 3, pp. 1599-1606, 1993.
- [49] Y. I. Ismail and E. G. Friedman, *On-chip inductance in high speed integrated circuits*. Springer Science & Business Media, 2001.
- [50] L. S. Gao *et al.*, "On-the-spot lung cancer differential diagnosis by label-free, molecular vibrational imaging and knowledge-based classification," *Journal of biomedical optics*, vol. 16, no. 9, p. 096004, 2011.
- [51] J. Kim, D. Kim, Y. Cho, D. Kang, B. Park, and B. Kim, "Envelope-tracking two-stage power amplifier with dual-mode supply modulator for LTE applications," *IEEE Transactions on Microwave Theory and Techniques*, vol. 61, no. 1, pp. 543-552, 2012.
- [52] M. Mondal *et al.*, "Thermally robust clocking schemes for 3D integrated circuits," in *2007 Design, Automation & Test in Europe Conference & Exhibition*, 2007: IEEE, pp. 1-6.
- [53] A. Nieuwoudt and Y. Massoud, "Understanding the impact of inductance in carbon nanotube bundles for VLSI interconnect using scalable modeling techniques," *IEEE Transactions on Nanotechnology*, vol. 5, no. 6, pp. 758-765, 2006.
- [54] C.-W. Wang, T.-G. Ma, and C.-F. Yang, "A new planar artificial transmission line and its applications to a miniaturized butler matrix," *IEEE Transactions on Microwave Theory and Techniques*, vol. 55, no. 12, pp. 2792-2801, 2007.