

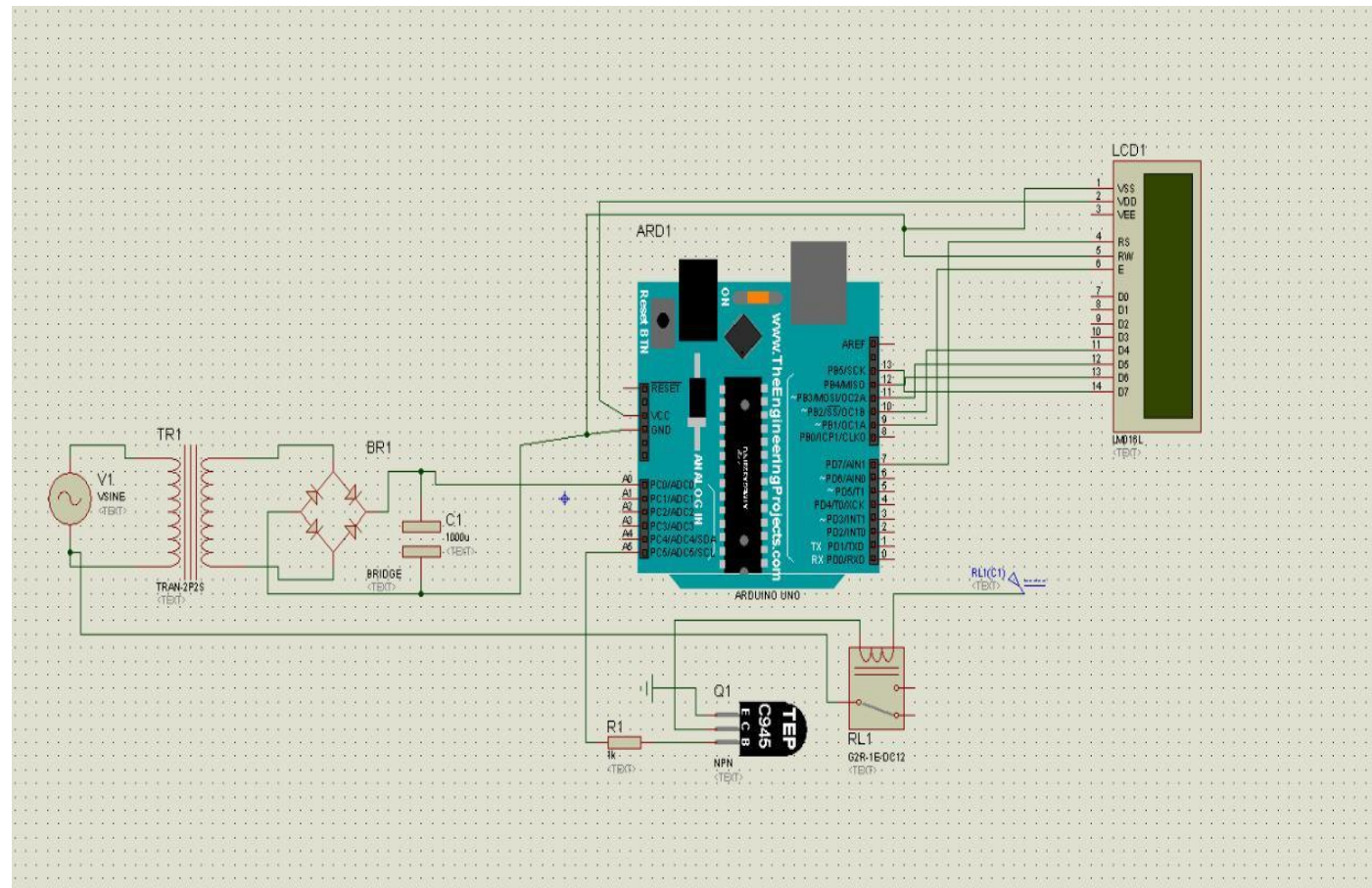


Figure. 5. Circuit Diagram